Notice of Allowability	Application No.	Applicant(s)	
	10/568,556	GOFF ET AL.	
	Examiner	Art Unit	
	CAROLINE ARCOS	2195	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *06/05/2009*.

2. ☒ The allowed claim(s) is/are *1, 3, 5, 7, 10 and 11, are now renumbered to claims 1-6*.

3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
   a) ☒ All b) ☐ Some* c) ☐ None of the:
      1. ☒ Certified copies of the priority documents have been received.
      2. ☐ Certified copies of the priority documents have been received in Application No. _____ .
      3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

   * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
   (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
      1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
   (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

   Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date *09/23/2009* .
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

/Meng-Ai An/
Supervisory Patent Examiner, Art Unit 2195

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

2. Authorization for this examiner's amendment was given in a telephone interview with Laurence stein on 09/23/2009.

3. Please replace all prior versions or listings of the application claims with the following:

1. (Currently Amended) A computer-based software task management system comprising:

a Task ID register for storing a plurality of different Task IDs;

a plurality of index registers, each associated with a corresponding one of the Task IDs, each index register configured to store a data register pointer for pointing to a data register;

a Task ID memory coupled to the Task ID register and configured to store a plurality of flags, each flag associated with a corresponding one of the Task IDs and each indicating whether the corresponding Task ID is available; ~~and~~

a state machine coupled to the Task ID memory and configured to (a) receive a Task ID request from a task, (b) to determine whether a Task ID is available in response

to the Task ID request, (c) when a Task ID is available, to issue ~~a~~ the Task ID to the task and set the flag in the Task ID memory indicating that the Task ID is in use, and (d) when the task is complete, to reset the flag in the Task ID memory indicating that the Task ID is available ~~.~~ ;

wherein the Task ID register comprises a plurality of Task ID registers each configured to store one of said Task IDs;

wherein the Task ID memory comprises a plurality of Task ID memories, each storing one of said plurality of flags each coupled to the Task;

wherein the state machine is configured to manage a plurality of tasks with the plurality of index registers, Task ID registers and the Task ID memory;

wherein a flip-flop circuit is coupled to each index register and is configured to toggle in response to each instance of the task, to cause the task to alternate between a write cycle to the index register and one selected from the group consisting of: a write cycle to the data register pointed to by the index register and a read cycle to the data register pointer to by the index register.

2. (Cancelled)

3. (Original) The computer-based software task management system of claim ~~2~~ 1, wherein each said index register is uniquely associated with a different Task ID.

4. (Cancelled)

5. (Currently Amended) A computer-implemented method for managing multiple tasks

using a state machine coupled to a Task ID memory having a plurality of Task ID memories, coupled to a plurality of Task ID registers, each of the Task ID registers configured to store one of a plurality of Task IDs, and a plurality of index registers, each ~~an~~ index register associated with a corresponding Task ID and configured to store a data register pointer for pointing to a data register, comprising:

storing one of a plurality of flags in each of the Task ID memories, each flag associated with the Task ID stored by the Task ID register coupled to the Task ID memory;

~~(a)~~ receiving a Task ID request from a task at the state machine;

~~(b) reading a Task ID memory in response to the Task ID request to determine whether a Task ID is available for the Task ID request;~~

~~(c) when a Task ID is available, issuing a Task ID to the task and setting a flag in the Task ID memory indicating that the Task ID is in use; and~~

~~(d) when the task is complete, resetting the flag in the Task ID memory indicating that the Task ID is available.~~

determining whether a Task ID is available for the Task ID request, said determining comprising reading the flags stored in the Task ID memory in response to the Task ID request to determine, based on the flags, whether a Task ID is available for the Task ID request;

in response to said determining that said Task ID is available, issuing said Task ID to the task and setting the flag associated with the issued Task ID in the Task ID memory to indicate that the Task ID is in use;

detecting when the task having the issued Task ID is complete and, in response, resetting the flag associated with said Task ID in the Task ID memory to indicate that the Task ID is available; and

alternating a read/write flip flop toggle in response to each instance of the task, causing the task to alternate between a write cycle to the index register and one selected from the group consisting of: a write cycle to the data register pointed to by the index register; and a read cycle to the data register pointed to by the index register.

6. (Canceled)

7. (Currently Amended) The computer-implemented method of claim 5, further comprising: in response to the determining ~~a~~ the Task ID is not available, periodically requesting ~~a~~ the Task ID.

8. (Cancelled)

9. (Cancelled)

10. (Currently Amended) The computer-implemented method of claim ~~8~~ 5, further comprising:

resetting the read/write flip-flop after each read-cycle, the flip-flop steering the read and write accesses to the index register and the data register pointed to by the index register.

11. (Currently Amended) The computer-implemented method of claim ~~9~~ 7, further

comprising: resetting the read/write flip-flop after each read-cycle, the read/write flip-flop steering the read and write accesses to the index register and the data register pointed to by the index register.

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to CAROLINE ARCOS whose telephone number is (571)270-3151. The examiner can normally be reached on Monday-Thursday 7:00 AM to 5:30 PM.

5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng-Ai An can be reached on 571-272-3756. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Caroline Arcos/
Examiner, Art Unit 2195

/Meng-Ai An/
Supervisory Patent Examiner, Art Unit 2195